

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage)).clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L2	1	(register and evaluation and storage and keeper and (output adj circuit)). clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L3	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node)).clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L4	1	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage)).clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L5	1	(register and evaluation and storage and keeper and (output adj circuit)). clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L6	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node)).clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L7	1	(register\$3 and logic and (pre-setting) and evaluat\$3 and maintain\$3 and (output adj node) and (output adj stage)).clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L8	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node)).clm.	US-PGPUB	OR	ON	2006/10/12 14:27
L9	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node)).clm.	US-PGPUB	OR	ON	2006/10/12 14:28
L10	1	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage)).clm.	US-PGPUB	OR	ON	2006/10/12 14:28
L11	1	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage)).clm.	US-PGPUB	OR	ON	2006/10/12 14:28
L12	1	(register and evaluation and storage and keeper and (output adj circuit)). clm.	US-PGPUB	OR	ON	2006/10/12 14:28
L13	1	(register\$3 and logic and (pre-setting) and evaluat\$3 and maintain\$3 and (output adj node) and (output adj stage)).clm.	US-PGPUB	OR	ON	2006/10/12 14:28
L14	1	(register and evaluation and storage and keeper and (output adj circuit)). clm.	US-PGPUB	OR	ON	2006/10/12 14:28

EAST Search History

L15	3	1 or 2 or 3 or 4 or 5 or 6 or 7 or 8 or 9 or 10 or 11 or 12 or 13 or 14	US-PGPUB	OR	ON	2006/10/12 14:29
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EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage))	US-PGPUB	OR	ON	2006/10/12 14:43
L2	18	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB	OR	ON	2006/10/12 14:43
L3	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB	OR	ON	2006/10/12 14:44
L4	5	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage))	US-PGPUB	OR	ON	2006/10/12 14:43
L5	18	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB	OR	ON	2006/10/12 14:44
L6	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB	OR	ON	2006/10/12 14:38
L7	17	(register\$3 and logic and (pre-setting) and evaluat\$3 and maintain\$3 and (output adj node) and (output adj stage))	US-PGPUB	OR	ON	2006/10/12 14:44
L8	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB	OR	ON	2006/10/12 14:44
L9	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB	OR	ON	2006/10/12 14:38
L10	5	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage))	US-PGPUB	OR	ON	2006/10/12 14:45
L11	18	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB	OR	ON	2006/10/12 14:45
L12	17	(register\$3 and logic and (pre-setting) and evaluat\$3 and maintain\$3 and (output adj node) and (output adj stage))	US-PGPUB	OR	ON	2006/10/12 14:45
L13	18	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB	OR	ON	2006/10/12 14:45
L14	3	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB	OR	ON	2006/10/12 14:45
L15	36	1 or 2 or 3 or 4 or 5 or 6 or 7 or 8 or 9 or 10 or 11 or 12 or 13 or 14	US-PGPUB	OR	ON	2006/10/12 14:46

EAST Search History

L16	6	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:43
L17	33	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:43
L18	6	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:44
L19	4	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:44
L20	33	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:44
L21	26	(register\$3 and logic and (pre-setting) and evaluat\$3 and maintain\$3 and (output adj node) and (output adj stage))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:44
L22	4	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:45

EAST Search History

L23	6	((domino adj register) and logic and evaluation and storage and keeper and (output adj stage))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:45
L24	33	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:45
L25	26	(register\$3 and logic and (pre-setting) and evaluat\$3 and maintain\$3 and (output adj node) and (output adj stage))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:45
L26	33	(register and evaluation and storage and keeper and (output adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:45
L27	4	(registering and logic and (pre-setting) and evaluating and maintaining and (output adj node))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:45
L28	62	16 or 17 or 18 or 19 or 20 or 21 or 22 or 23 or 24 or 25 or 26 or 27	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:46
L29	26	28 not 15	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:47

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2306	(dynamic adj logic) or (domino adj logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:32
S2	424	(dynamic adj register) or (domino adj register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:32
S3	2719	S1 or S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S4	1258	S3 and (keeper or latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S5	797	S4 and (precharge or pre-charge)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S6	733	S5 and clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S7	607	S6 and evaluat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S8	12	("20020158670" "20030042932" "20030052714" "20030110404" "5075386" "5889979").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 08:04

EAST Search History

S9	0	("2004/0257115").URPN.	USPAT	OR	OFF	2005/05/27 08:19
S10	786	lundberg.inv.	USPAT	OR	OFF	2005/05/27 08:21
S11	0	lundberg-james.inv.	USPAT	OR	OFF	2005/05/27 08:20
S12	1	lundberg-james.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/27 08:20
S13	0	S10 and 326/95.ccls.	USPAT	OR	OFF	2005/05/27 08:21
S15	5655	bertram.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/27 08:21
S16	2	bertram-raymond.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/27 08:22
S17	4	("5012246" "5459421" "5619157" "5712826").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/27 09:27
S18	10	("5973529").URPN.	USPAT	OR	OFF	2005/05/27 09:28
S19	2	("5886540" "5983013").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/27 10:11
S20	2	("6650145").URPN.	USPAT	OR	OFF	2005/05/27 10:12
S21	2665	(dynamic adj logic) or (domino adj logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:32
S22	496	(dynamic adj register) or (domino adj register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:32
S23	2665	(dynamic adj logic) or (domino adj logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33

EAST Search History

S24	496	(dynamic adj register) or (domino adj register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S25	3139	S23 or S24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S26	3139	S23 or S24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S27	1467	S26 and (keeper or latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S28	1467	S26 and (keeper or latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S29	943	S28 and (precharge or pre-charge)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S30	943	S28 and (precharge or pre-charge)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S31	876	S30 and clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33

EAST Search History

S32	876	S30 and clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:33
S33	724	S32 and evaluat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:34
S34	724	S32 and evaluat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 14:34